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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/085,009 03/01/2002 PAT 6,727,120 *yes LP*

** FOREIGN APPLICATIONS *****

JAPAN 2001-063491 03/07/2001 *yes LP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 29	INDEPENDENT CLAIMS 6	
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance					
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials			

ADDRESS

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TITLE

Wiring method in layout design of semiconductor integrated circuit, semiconductor integrated circuit and functional macro

FILING FEE RECEIVED 1190	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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